

Comments on the exam 031022

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Correct answers for the questions are found in the textbook. Individual results are posted on my office door (1142) and you can get the graded exams at institutionskansliet.

A few details on some of the questions:

2. Microinstructions must be easy to decode since they are interpreted by hardware.

12. 0xffab.

13. Many made the distinction that L1 cache is on the same chip as the CPU while the L2 cache is in a different chip. Since this impression can easily be made from the textbook I count it as correct in the grading, even though in fact it is not correct: many modern CPUs have both L1 and L2 cache on the CPU chip.

25. The correct answer is that each microinstruction contains the address of its successor, a fact surprisingly few got right.

Result:

27 persons sat the exam and 19 persons (i.e 70%) passed. The mean result was 22.2 points and the median 22. Of those who did not pass several are quite close to passing and I hope you will try again. Next possibility to take the exam will be on the distance course (exam date Friday 19 December).

The distribution of results is as follows:

